

NOW, THEREFORE, for good and valuable consideration the sufficiency of which is hereby acknowledged, Honeywell hereby agrees and does sell, assign and transfer unto LG Electronics, subject to any existing license agreements under the Assigned Patent Rights, which existing license rights shall be retained by the restrictive parties to said license agreements, all right, title and interest of Honeywell in and to the Assigned Patent Rights, such rights to include all rights for past and future damages and any other such relief as may arise from past or future infringements of any patents within said Assigned Patent Rights.

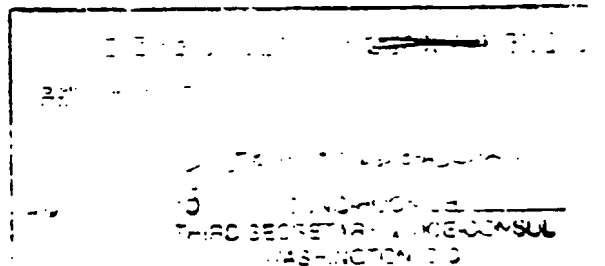
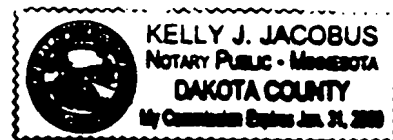
Honeywell Inc.

By: *D. Z. Moore*

On this 23 day of ~~September~~^{October} 1995, appeared before me D. Larry Moore to me known and personally known to me to be the individual referred to in the forgoing assignment, and he acknowledged to me that he executed the same as his free act and deed for the use and purposes therein set forth.

Executed this 23 day of ~~September~~^{October} 1995.

Kelly J. Jacobus
Notary Public, County Dakota
State of Minnesota
My Commission Expires Jan. 31, 2000



95 3231



File No.: ALPHA-18

ASSIGNMENTINVENTOR/ASSIGNOR: Scott H. HolmbergResidence Address 408 Old Ranch CourtCity San Ramon County Contra Costa State CaliforniaASSIGNEE: Alphasil, Inc.Status California
(Corporation of what state, firm or individual)Address 1045 Mission CourtCity and State Fremont, California 94539NAME OF INVENTION: METHOD OF MANUFACTURING FLAT PANEL BACKPLANES
INCLUDING ELECTROSTATIC DISCHARGE PREVENTION
AND DISPLAYS MADE THEREBY

The Assignor having made the above invention and filed application for Letters Patent of the United States thereon, and the Assignee being desirous of acquiring the same; in consideration of One Dollar (\$1.00) and other good and valuable considerations, the Assignor hereby assigns to the Assignee, including its successors, assigns, heirs, administrators, all of the Assignor's right, title and interest in and to the invention and the patent application therefor identified herein and to any and all patents which may evolve therefrom;

And the Assignor also assigns all of his right, title and interest in and to said invention in all foreign countries, and all applications for Letters Patent which may evolve therefrom including the right to claim International Convention priority;

And Assignor agrees to execute any papers or perform any acts required to establish, vest or protect the Assignee's rights therein or required by Assignee to obtain said patents, without any additional payment therefor but without any expense to Assignor.

DATE OF THIS DOCUMENT September 8, 1988

The date of execution of the U.S. application is the same as the date of execution of this document unless stated otherwise here

SERIAL NO. 07/218,312 FILING DATE July 12, 1988

SEP 12 80

State of: CaliforniaCounty of: Alameda

(seal)

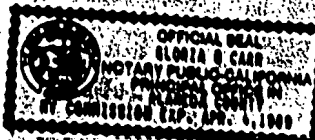
Scott H. Holmberg

Scott H. Holmberg personally appeared before me on the date above mentioned and acknowledged his signing, sealing and delivery of this instrument as a free and voluntary act for the purposes set forth.

(seal)

Notary Public

My commission expires:

04/04/89

EIN 4935 ERM081

ASSIGNMENT OF PATENTS AND
PATENT APPLICATIONS

Whereas Alphasil, Inc., a corporation organized and existing under the laws of the State of California (hereinafter referred to as "Assignor") and Honeywell Inc., a corporation organized and existing under the laws of the State of Delaware, having its principal place of business at Minneapolis, Minnesota, its address being Honeywell Plaza, Minneapolis, MN 55408 (hereinafter referred to as "Assignee") are presently the co-owners as shown by documents on file in the United States Patent and Trademark Office of the entire right, title and interest in and to the following United States Patents:

- (a) Patent No.: 4,736,229
Date of Issue: April 5, 1988
Title: Method of Manufacturing Flat Panel Backplanes, Display Transistors and Displays Made Thereby
- (b) Patent No.: 4,651,185
Date of Issue: March 17, 1987
Title: Method of Manufacturing Thin Film Transistors and Transistors Made Thereby
- (c) Patent No.: 4,545,112
Date of Issue: October 8, 1985
Title: Method of Manufacturing Thin Film Transistors and Transistors Made Thereby

(hereinafter referred to as the "Patents"); and

Whereas the inventors of the Patents are Scott H. Holmberg and Richard A. Flasck; and

Whereas Assignor is presently the owner and/or the co-owner with Assignee of the entire right, title and

RM 5080 RM 600

Interest in and to the following United States Patent
Applications:

- (a) Serial No.: 150,927
File Date: February 1, 1988
Inventors: Scott H. Holmberg and Richard A. Flasck
Title: Method of Manufacturing Flat Panel
Backplanes, Display Transistors and
Displays Made Thereby
- (b) Serial No.: 948,224
File Date: December 31, 1986
Inventors: Scott H. Holmberg and Richard A. Flasck
Title: Method of Manufacturing Flat Panel
Backplanes Including Improved Testing and
Yields Thereof and Displays Made Thereby
- Patent Application No. 948,224 has been granted by
the United States Patent and Trademark Office, and
as a result Patent No. 4,820,222 will issue on
April 11, 1989.
- (c) Serial No.: PCT/US87/03470
File Date: December 22, 1987
Inventors: Scott H. Holmberg and Richard A. Flasck
Title: Method of Manufacturing Flat Panel
Backplanes Including Improved Testing and
Yields Thereof and Displays Made Thereby
- (d) Serial No.: 218,312
File Date: July 12, 1988
Inventor: Scott H. Holmberg
Title: Method of Manufacturing Flat Panel
Backplanes Including Electrostatic
Discharge Prevention and Displays Made
Thereby
- (e) Serial No.: 84105383.8
File Date: May 11, 1984
Inventors: Scott H. Holmberg and Richard A. Flasck
Title: Method of Manufacturing Flat Panel
Backplanes, Display Transistors and
Displays Made Thereby
- (f) Serial No.: 035,515
File Date: April 7, 1987
Inventors: Richard A. Flasck, Benny Irwin and
Scott H. Holmberg
Title: Method of Illuminating Flat Panel
Displays To Provide CRT Appearing
Displays

FILE 5080 NAME 601

- (g) Serial No.: PCT/US88/01085
File Date: March 31, 1988
Inventors: Richard A. Plasck, Benny Irvin and
Scott H. Holmberg
Title: Method of Illuminating Flat Panel
Displays To Provide CRT Appearing
Displays
- (h) Serial No.: 242,776
File Date: September 12, 1988
Inventors: Richard A. Plasck and Thomas D. Stahl
Title: Liquid Crystal Screen for Overhead
Projector

(hereinafter referred to as the "Patent Applications"); and

~~Whereas~~ Assignor and Assignee have entered into an Agreement Regarding Assignment of Patents, Patent Applications and Related Intellectual Property; Providing for Sale of Equipment Leased from Valley Bank, dated as of April 4, 1989 ("Assignment Agreement");

N o w, T h e r e f o r e, in consideration of the premises, terms and conditions contained in said Assignment Agreement, and for other good and valuable consideration as has been mutually agreed upon between the parties, Assignor hereby sells, assigns, transfers and sets over to Assignee:

(a) All of Assignor's right, title and interest in and to the Patents, together with and including all reissues and extensions thereof, the same to be held and enjoyed by the Assignee for its own use and enjoyment and the use and enjoyment of its successors, assigns or other legal representatives; and

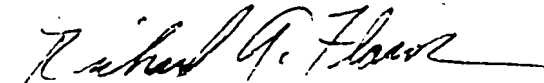
(b) All of Assignor's right, title and interest in and to the Patent Applications, together with and including any patent or patents which may issue thereunder, the same to

be held and enjoyed by the Assignee for its own use and enjoyment and the use and enjoyment of its successors, assigns or other legal representatives; and

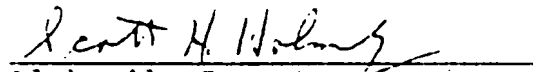
(c) All causes of action, past and present and future for unauthorized use, infringement, or unfair competition with respect to said Patents and/or Patent Applications.

Assignor further covenants and agrees that it will, at any time, upon request, execute and deliver any and all papers that may be necessary or desirable to perfect the title to said Patents and/or Patent Applications in said Assignee, its successors, assigns or other legal representatives, and that it will, at any time upon request, communicate to said Assignee, its successors, assigns or other legal representatives such facts relating to said Patents and/or Patent Applications or the history thereof as may be known to it.

Dated as of the 4 day of April, 1989.


Alphasil, Inc.

By Richard A. Flasck
Its President


Alphasil, Inc.

By Scott H. Holmberg
Its Vice President

[Corporate Seal]

ACKNOWLEDGMENT

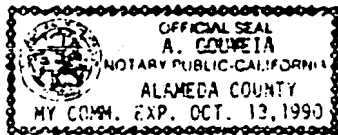
APR -6 89

STATE OF CALIFORNIA,)
) ss.
County of ALAMEDA .)

[Signature]
NOTARY PUBLIC

On this 4th day of APRIL, 1989, before me personally appeared Richard A. Flasck and Scott H. Holmberg, known to me to be the President and Vice President of the corporation that executed the within and foregoing instrument, and acknowledged the said instrument to be the free and voluntary act and deed of said corporation, for the uses and purposes therein mentioned, and on oath stated that they were authorized to execute said instrument and that the seal affixed is the corporate seal of said corporation.

In witness whereof, I have hereunto set my hand and affixed my official seal as of the day and year first above written.



[Signature]
Notary Public in and for the
State of California, residing at

1415 HILLVIEW DR. FARMINGTON, CT

My commission expires: 10-13-1990

REEL 5080 FRAMES 014

SCHEDULE C

Assignment

WHEREAS, Honeywell Inc. ("Honeywell"), a Delaware corporation having its principal place of business at Honeywell Plaza, Minneapolis, Minnesota 55408 owns a portfolio of United States patents identified as follows:

- 4,736,229 Method of Manufacturing Flat Panel Backplanes, Display Transistors and Displays Made Thereby
- 4,651,185 Method of Manufacturing Thin Film Transistors and Transistors Made Thereby
- 4,820,222 Method of Manufacturing Flat Panel Backplanes Including Improved Testing and Yields Thereof and Displays Made Thereby
- 4,842,378 Method of Illuminating Flat Panel Displays to Provide CRT Appearing Displays
- 5,019,002 Method of Manufacturing Flat Panel Backplanes Including Electrostatic Discharge Prevention and Displays Made Thereby
- 5,162,931 Method of Manufacturing Flat Panel Backplanes Including Redundant Gate Lines and Displays Made Thereby
- 5,123,847 Method of Manufacturing Flat Panel Backplanes, Display Transistors

hereby made a part hereof together with their corresponding foreign filed counterpart patent applications and issued patents and all their continuations, divisionals, reissues and renewals (hereinafter "Assigned Patent Rights"); and

WHEREAS, LG Electronics Inc. ("LG Electronics"), a corporation having its principal place of business at LG Twin Tower, 20, Yoido-Dong, Youngdungpo-Gu, Seoul, Korea is desirous of obtaining the entire right, title and interest of Honeywell in and to said Assigned Patent Rights; and

WHEREAS, Honeywell and LG Electronics have executed an Agreement dated ^{OCTOBER} September 23, 1995, with respect to the sale and assignment of the Assigned Patent Rights.